

Form PTO-1449

MAY 21 2001

Attorney Docket No.
052102-1011Serial No.
09/783,886

INFORMATION DISCLOSURE CITATION

Applicant
Luis R. LopezFiling Date
February 12, 2001Group
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U.S. PATENT DOCUMENTS

Examiner Initials	Item	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
TTV	A	4,607,339	08/19/86	Davis, et al.	364	491	06/27/83
TTV	B	4,827,404	05/02/89	Barstow, et al.	364	200	04/14/86
TTV	C	4,831,525	05/16/89	Saito, et al.	364	300	01/19/88
TTV	D	4,831,580	05/16/89	Yamada	364	900	07/10/86
TTV	E	4,852,047	07/25/89	Lavallee, et al.	364	191	04/14/87
TTV	F	4,922,432	05/01/90	Kobayashi, et al.	364	490	01/13/88
TTV	G	5,187,788	02/16/93	Marmelstein	395	700	06/27/91
TTV	H	5,197,016	03/23/93	Sugimoto, et al.	364	490	07/14/89
TTV	I	5,428,550	06/27/95	Covey, et al.	364	489	06/04/92
TTV	J	5,430,873	07/04/95	Abe, et al.	395	650	09/14/92
TTV	K	5,446,911	08/29/95	Juso, et al.	395	800	08/29/95
TTV	L	5,555,357	09/10/96	Fernandes, et al.	395	141	09/10/96
TTV	M	5,729,747	03/17/98	Tsukakoshi	395	702	03/17/98

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation
							Yes No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

TTV	N	"On-Line Graphical Specification of Computer Procedures," W.F. Sutherland, MIT, Cambridge, MA, 1966					
TTV	O	"Structure of a Source Program Generator," IBM Technical Disclosure Bulletin, Vol. 14, Issue: 1, pp. 306-311, Figure, June 1971,					
TTV	P	"Programming Tool for Automated Flow Chart Generation of Assembler Language Programs" IBM Technical Disclosure Bulletin, Vol. 15, Issue: 10, pp. 2999-3001, Figure, March 1973.					
TTV	Q	"Global Insertion and Iterative Refinement of Breakpoints in Logic Synthesis Verification," IBM Technical Disclosure Bulletin, Vol. 33, Issue: 6B, pp. 54-57, Figure, November 1990.					
TTV	R	"Automated Code Generation from Graphical, Reusable Templates," Dellen, Liebner, pp. 299-304, 1991 IEEE.					

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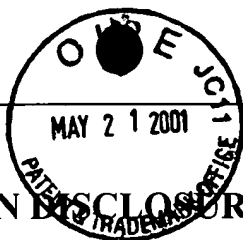
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							Yes No

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TTV	S	"LEX & YACC," O'Reilly & Associates, Inc., ISBN:1-56592-000-7, pp. 1-3, 1992.
TTV	T	"Method to Perform Global Boolean Equivalents check at the Chip Level", IBM Technical Disclosure Bulletin, Vol. 36, Issue: 6A, pp. 395-396, Figure, June 1993.
TTV	U	"A-Classification System for Visual Programming Languages," Burnett, Margaret M. and Marla J. Baker, Journal of Visual Languages and Computing, 287-300, September 1994.
TTV	V	Article "Programming in Three Dimensions", Thesis of Mark Najork, Univ. of Ill., pp. 21-76, 1994, is a cumulative of article "Visual Programming in 3D," Marc Najork, Dr. Dobb's Journal, #237, Miller Freeman, December 1995.
TTV	W	"A Generator for Diagram Editors Providing Direct Manipulation and Execution of Diagrams," Minas and Viehstaedt, pp. 203-210, 1995 IEEE.
TTV	X	"Drawing Execution Graphs by Parsing," deBruyn and Roosamalen, pp. 113-122, January 1995 IEEE.
TTV	Y	"Retrieving Information from Data Flow Diagrams," Butler, et al., pp. 22-29, April, 1995 IEEE.
TTV	Z	"Why Explore Object Methods, Patterns, and Architectures," IEEE Software, pp. 1-8, January 1997.
TTV	AA	"The Flowchart Interpreter for Introductory Programming Course," Thad Crews and Uta Ziegler, pp. 307-312, 1998 IEEE Conference.
TTV	AB	"Get Up to Speed with XML," Boris Feldman, XML, Fawcette Technical Publication, Winter 1999/2000, Vol. 1, No. 1, December 1999, pp. 1-5.
	AC	W3C Architecture Domain - Various documents from the World Wide Web * see note

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* : References are not considered because there is no date of publication

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